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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Tsuyoshi YAMAMOTO et al.**

Group Art Unit: **3729**

Application Number: **10/073,106**

Examiner: **Paul D. Kim**

Filed: **February 12, 2002**

Confirmation Number: **3709**

For: **METHOD OF MOUNTING ELECTRONIC COMPONENT ON  
SUBSTRATE WITHOUT GENERATION OF VOIDS IN BONDING  
MATERIAL**

Attorney Docket Number: **020154**

Customer Number: **38834**

**REQUEST FOR RECONSIDERATION AFTER FINAL REJECTION**

**MAILSTOP: AF**  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

May 8, 2006

Sir:

This paper is filed in response to the Office Action dated February 9, 2006.

Remarks begin on page 2 of this paper.